


Impinj IPJ-E4000  
23 dBm Reader SIP  
Development PCBA

Generic PCBA  
P/N 100-1166

Impinj Revision	Date	Description
1.0		Initial design
2.0		Added USB-dual UART IC and updates from initial design
2.01		Changed net names regarding host and debug
2.02		Added new RS500 with 4 more grounds and renumbered pins to make them sequential counterclockwise. Also renumbered GPIO's starting from 1 vs 0
2.03		Reverted back to RS500 without renumbering the pins.
2.04		Changed J5 to a 2x5 vs 2x4, and removed 3.3V from that header to facilitate FCT with hidden glen fixture.
2.05		Broke up schematic into two pages.
2.06		Added provisions for allowing USB to Uart translator to control nReset and Wake up signals. Cleaned up some n.m. resistors.

	Title 01-COVER SHEET	
	Size B	Document Number 100-1166
	Impinj Rev 2.06	
Date: Tuesday, March 25, 2014		Sheet 1 of 3

