



Monza Product Change Notice

December 19th, 2014

Monza 4 UMI bit function change

Dear Customer,

Impinj is making a minor change to Monza 4 products (Monza 4D IPJ-W1513-xxx, Monza 4E IPJ-W1510-xxx and Monza 4QT IPJ-W1512-xxx) to comply with UMI bit functionality in the Gen2V2 specification. Currently in Monza 4, the value of the UMI bit in the EPC memory bank is computed based on presence or absence of data in the user memory. After the change, the UMI bit of Monza 4 products will be set to a default value of 1 to indicate presence of user memory bank. Only for Monza 4QT, the UMI bit value will change to 0 automatically when it is configured to Public Mode indicating no user memory.

This change does not affect the RF front end and hence will not cause any change to performance or manufacturability of current inlay designs. There is also no change to the order part numbers or the TID Model Numbers. Based on current market demand, we expect to start shipping Monza 4D wafers with this change around the beginning of February 2015 while Monza 4E and Monza 4QT wafers with this change are expected to start shipping in April 2015. Specific dates will vary based on changing market demand.

While this change is completely transparent to inlay manufacturing, converting and encoding operations it could affect a small number of end-users who might be using current UMI bit functionality in Monza 4 products. We encourage you to notify your customers and end-users about this change so they can plan for any required changes to software. Such end-users who need to be certain of the UMI bit functionality of their Monza 4 tag can check the TID Mask Revision Number which will be updated from current value of 5 to new value of 7.

If you have further questions about these changes, you may contact Impinj by sending an email to support@impinj.com.

Thank you
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